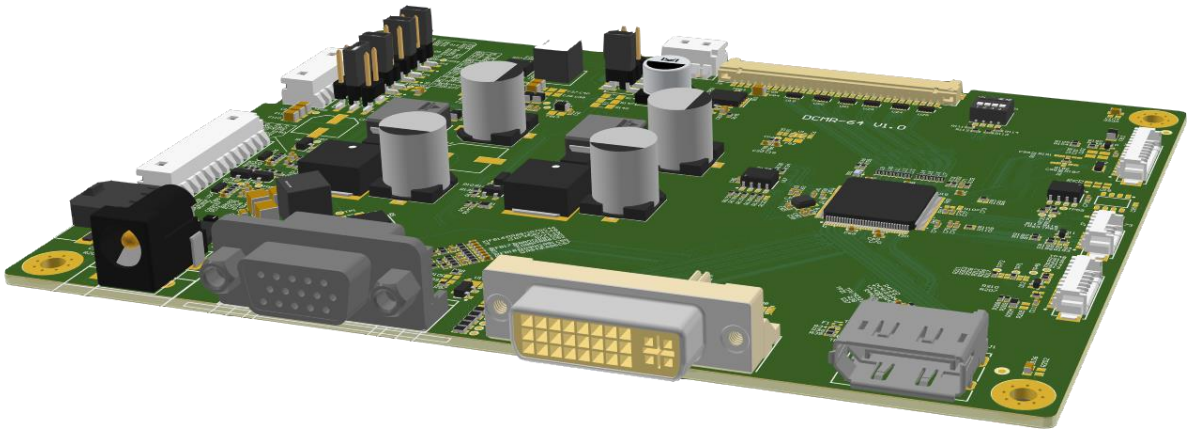
DCMR-64

(DisplayPort, DVI-I and RGB TFT-LCD Controller)

Product Specification



Product Specification Status
- ○ Preliminary
- ✓ Final

This specification is subject to legal disclaimers.

1. Product Overview

This DisplayPort, DVI-I and analog RGB to LVDS converter board accepts common video signals. DisplayPort 1.2, DVI 1.0, HDMI 1.4a and all backward compatible signals are supported as well as analog RGB. It generates all necessary control signals and panel data to drive TFT-LCDs with VDD levels 3.3V, 5V or 12V. This TFT-controller board supports resolutions up to 1920x1200 (WUXGA) at a vertical refresh rate up to 60Hz. The user interface includes backlight, brightness, contrast, etc. adjustment by on-screen programming. For automatic backlight adjustment an ambient light sensor is supported. Several OSD functions can be managed via embedded I²C-interface or DDC/CI.

This board can easily be configured for different panels by the user (s. User's Guide), and it is available in different assembly configurations.

2. Features

General

- Support up to 1920x1200 (WUXGA) resolution @ 60Hz
- DDC/CI support by embedded DDC I²C-bus Interface
- I2C-slave interface for display control
- Panel usage timer
- On-board status LED

Display Configuration

- All voltages selectable by user[1]
- Display resolution selectable by user[1]

Power Management

- Input voltage from 12V to 24V[1]
- Reverse voltage protection[1]
- TFT panel power supply of 3.3V, 5V or 12V
- Backlight power supply 5V, 12V or 24V

DisplayPort 1.2 Digital Input Interface

- High-Bandwidth Digital Content Protection (HDCP v1.4) support
- Supports 6-bit, 8-bit, 10-bit, and 12-bit color depth transport

HDMI 1.4a Compliant Digital Input Interface

- Single link on-chip TMDS receiver up to 225MHz
- High-Bandwidth Digital Content Protection (HDCP v1.4) support
- Supports DVI 1.0

Analog RGB Input Interface

- Supports Sync-On-Green (SOG) and various kinds of composite sync modes
- YPbPr support up to 1080p

Auto Detection / Calibration

- Input format detection
- Compatibility with standard VESA Mode
- Smart engine for phase / image position / color calibration

Scaling

- Fully programmable zoom ratios
- Independent horizontal/vertical scaling
- Advanced zoom algorithm provides high image quality

[1] In var. STD

- Sharpness/Smooth filter enhancement
- Support non-linear scaling from 4:3 to 16:9 or 16:9 to 4:3

Color Processor

- True 10-bit color processing engine
- sRBG compliance

Output Interface

- Dual-LVDS 24-bit output interface
- Spread-Spectrum DPLL to reduce EMI
- Support VESA and JEIDA mapping

On Screen Display menu

- Backlight dimming
- Color adjustments
- Several other settings

Audio

- I²S 8-channel support
- Embedded Audio DAC

Environmental

- ✓RoHS
- ✓REACH

3. Functional Diagram

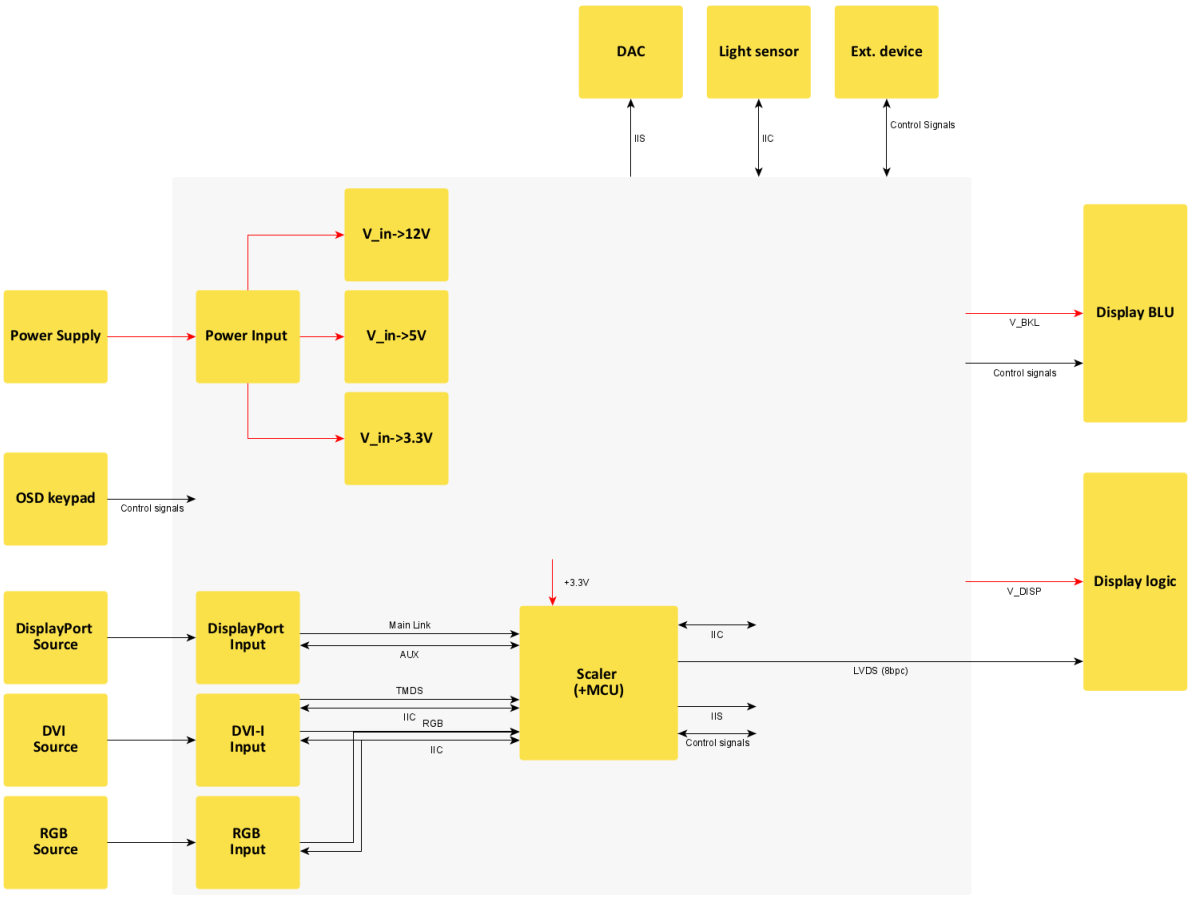


Fig 1. System Diagram

4. Typical Application

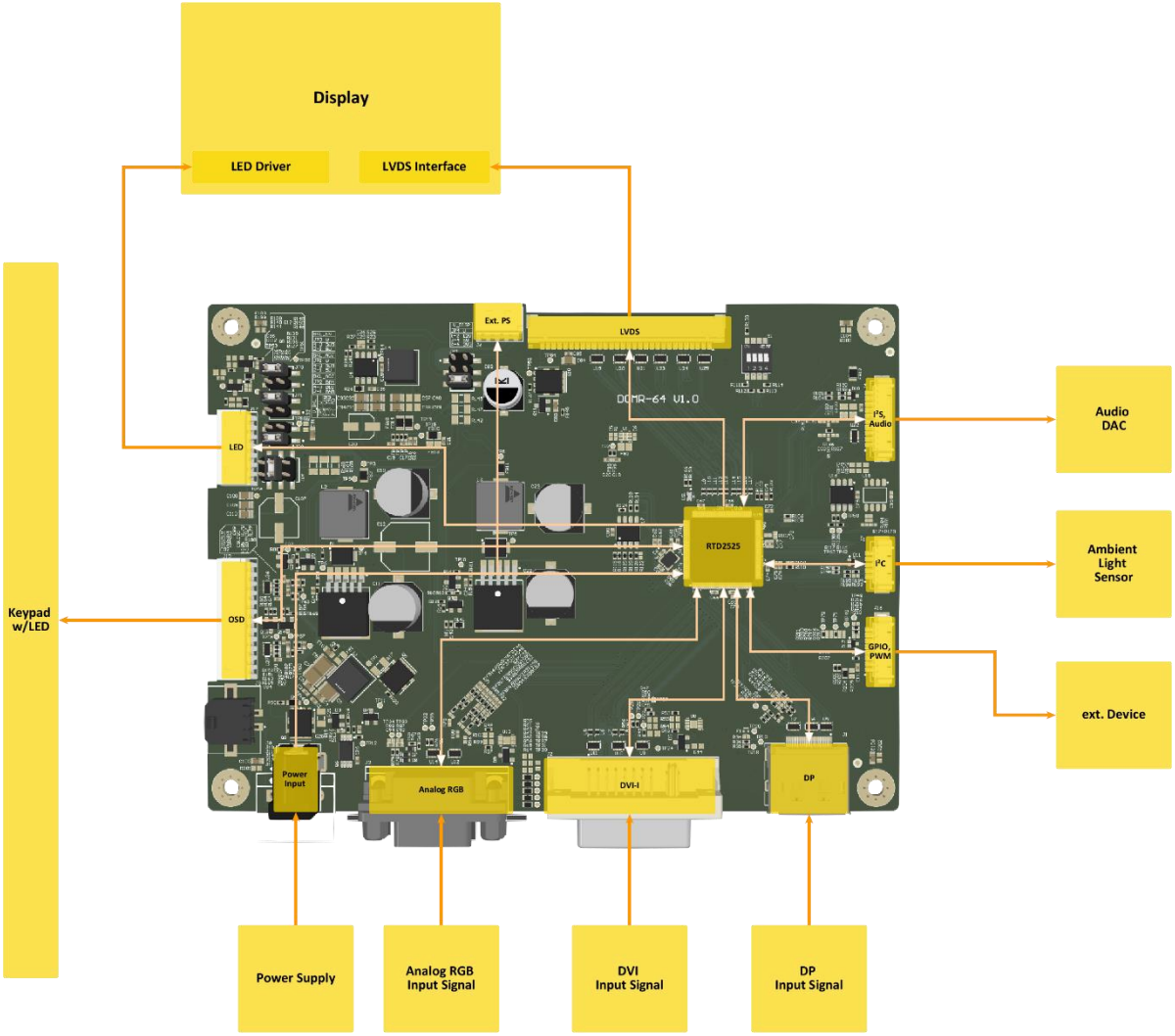


Fig 2. Application of DCMR-64

5. Electrical Characteristics

All ratings @ V_{DD} = 12.0V, ϑ = 25°C, var. STD, and voltages referenced to GND, unless otherwise noted.

a. Input

Table 1. Electrical Input Characteristics

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
V_{DD}	Operating Supply Voltage	Var. STD	10.8	-	26.4	V DC
		Var. Lite (12V cfg.)	10.8	12.0	13.2	V DC
		Var. Lite (24V cfg.)	15.0	24.0	26.4	V DC
V_{DDmax}	Absolute Max. Rating[2]	Var. STD	-35	-	35	V DC
		Var. Lite (12V cfg.)	-0.3	-	35	V DC
		Var. Lite (24V cfg.)	-0.3	-	35	V DC
I_{DD}	Current Consumption[3]	Board Only (active mode)	53	68	113	mA
		Board Only (sleep mode)	13	17	24	mA
		With WVGA Display[4]	0.13	0.24	0.37	A
		With XGA Display[5]	0.26	0.55	0.87	A
		With FHD Display[6]	0.39	1.10	1.72	A
P_{DD}	Power Consumption	Sleep mode	0.16	0.21	0.29	W
		Active mode	0.64	0.82	1.36	W

b. Output

DC Characteristics

Table 2. Electrical Output Characteristics

Symbol	Parameter	Test conditions	Min	Typ	Max	Unit
V_{Panel}	Display Supply Voltage	3.3V configuration	3.0	3.3	3.6	V DC
		5V configuration	4.5	5	5.5	V DC
		12V configuration	10.8	12	13.2	V DC
I_{Panel}	Display Supply Current	-	-	-	2.0	A

[2] Consider ratings of connected parts

[3] Current consumption depends on the board's firmware

[4] Test was performed with INX G070Y2-L01 Rev. C6 (WVGA, 500cd/m²). Backlight PWM duty ratio min: 10%

[5] Test was performed with INX G150XNE-L01 (XGA, 500cd/m²). Backlight PWM duty ratio min: 5%

[6] Test was performed with AUO G215HVN01.0 (FHD, 300cd/m²). Backlight PWM duty ratio min: 10%

V_{BKL}	Backlight Supply Voltage	5V configuration	4.5	5	5.5	V DC
		12V configuration	10.8	12	13.2	V DC
		V_{DD} configuration	-	V_{DD}	-	V DC
I_{BKL}	Backlight Supply Current	V_{DD} configuration	-	-	2	A
V_{BKL_EN}	Backlight Enable Voltage	3.3V configuration	3.0	3.3	3.6	V DC
		5V configuration	4.5	5	5.5	V DC
V_{BKL_ADJ}	Backlight Adjust Voltage	3.3V configuration	3.0	3.3	3.6	V DC
		5V configuration	4.5	5	5.5	V DC
f_{BKL_ADJ}[7]	Backlight Adjust Frequency	PWM configuration	160	200	20k	Hz
D_{BKL_ADJ}	Backlight Adjust Duty Cycle	PWM configuration	0	-	100	%

Panel Power Sequencing

DCMR-64 follows a special sequence to power up display- and backlight-VCC. This timing is set in the F/W.

6. OSD (On-Screen Display)

The user-friendly, intuitive controllable integrated OSD menu provides certain functions to change settings, adjust the image and others. It can be controlled by an OSD-keyboard. The status of the LCD controller can be checked by a LED which is integrated on the keyboard.

a. OSD-Keyboard

A 5-button OSD-keyboard with one LED on it to show the board's status is connectable.

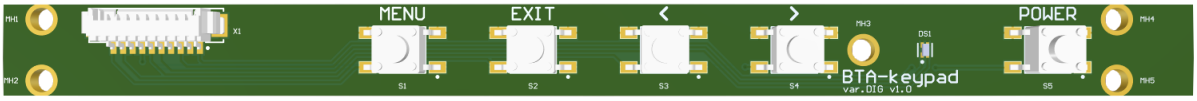


Fig 3. OSD-keyboard

Buttons

The function of each OSD key is shown in the following table.

Table 3. OSD keyboard functions

No.	Button	Switch Function	Hot-Key Function
1	Menu	1. Open OSD sub menu 2. Save changes and exit	Open OSD main menu
2	Exit	Discard changes and exit	-
3	Left	1. Move left in menu list	Show signal info

[7] Analog inverse output by hardware configuration.

		2. Decrease the value of selected item	
4	Right	1. Move right in menu list 2. Increase the value of selected item	Input select
5	Power	Turn power on / off	-

LED

LED colors and their meanings are described in the table below.

Table 4. OSD-keyboard LED status lights

LED Color	Status	Represents
Green	Constant	Normal state
Amber	Constant	Searching signal
Red	Constant	Power saving

b. OSD-menu

Menu Structure

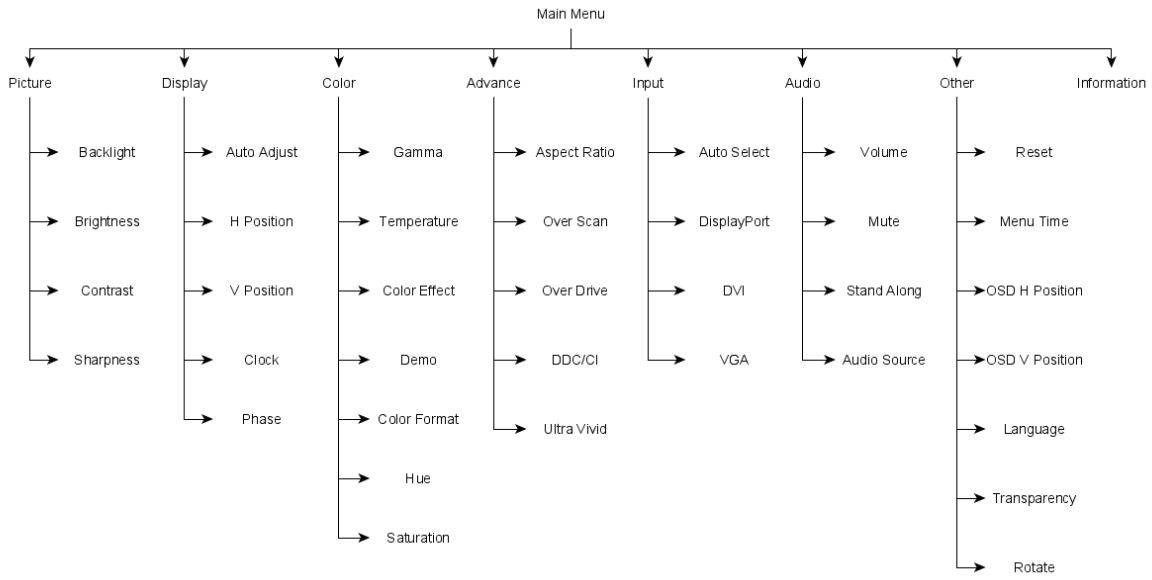


Fig 4. OSD-menu structure

Menu Items

Table 5. OSD: Menu Items

Title	Function
Picture	Adjust image settings
Display	Adjust image position
Color	Adjust color settings
Advance	Advanced settings
Input	Input source selection
Audio	Audio settings
Other	Adjust the On-Screen-Display settings
Information	Information about current timing

Main-Menu: Picture

Table 6. OSD: Main-Menu Picture

Title	Function
Backlight	Adjust the backlight-intensity
Brightness	Adjust the brightness of the image
Contrast	Adjust the contrast of the image
Sharpness	Adjust the sharpness of the image

Main-Menu: Display[8]

Table 7. OSD: Main-Menu Display

Title	Function
Auto Adjust	Input format detection, phase/image position adjustment
H Position	Adjust horizontal position of the image
V Position	Adjust vertical position of the image
Clock	Adjust clock
Phase	Adjust phase

Main-Menu: Color

Table 8. OSD: Main-Menu Color

Title	Function
Gamma	Adjust gamma level of the image
Temperature	Adjust the color temperature
Color Effect	Select a color effect
Demo	Select a mode to check hue and saturation settings
Color Format	Select the color model
Hue	Adjust the color balance
Saturation	Adjust the color intensity

Sub-Menu: Temperature - User

Table 9. OSD: Sub-Menu: Temperature - User

Title	Function
R	Adjust red video gain
G	Adjust green video gain
B	Adjust blue video gain

[8] Available for analog RGB

Sub-Menu: Color Effect - User

Table 10. OSD: Sub-Menu: Color Effect - User

Title	Function
Hue	Adjust hue of respective 6-axis color
Saturation	Adjust saturation of respective 6-axis color

Main-Menu: Advance

Table 11. OSD: Main-Menu Advance

Title	Function
Aspect Ratio	Select aspect ratio or scaling factor of the image
Over Scan	Enable / Disable overscan function
Over Drive	Enable / Disable overdrive function
DDC/CI	Enable / Disable DDC/CI function
Ultra Vivid	Select grades of Ultra Vivid color mode

Main-Menu: Input

Table 12. OSD: Main-Menu Input

Title	Function
Auto Select	Select first available input signal
DisplayPort	DisplayPort fixed
HDMI	HDMI fixed
RGB	RGB fixed

Main-Menu: Audio[9]

Table 13. OSD: Main-Menu Audio

Title	Function
Volume	Adjust audio volume
Mute	Mute audio output
Stand Along	Enable / Disable standby audio
Audio Source	Select audio source

Main-Menu: Other

Table 14. OSD: Main-Menu Other

Title	Function
Reset	Reset all OSD settings
Menu Time	Set the OSD duration [s]
OSD H Position	Set the horizontal position of OSD menu
OSD V Position	Set the vertical position of OSD menu
Language	Select OSD menu language
Transparency	Adjust the transparency of the OSD menu
Rotate	Rotate OSD by 0°, 90° or 270°

Main-Menu: Information

Table 15. OSD: Main-Menu Information

Title	Function
Signal source	Show current signal source
Current resolution	Show resolution of input signal
H- & V-frequency	Show horizontal and vertical frequency
Pixel clock frequency	Show pixel clock frequency
Board model	Board model
Firmware name	Firmware name
Firmware version	Firmware version

[9] Only for analog audio output.

7. Interfaces

a. LVDS Interface

At the LVDS-Interface, single- and dual-channel LVDS is supported in either 6- or 8-bit configuration. As the board is delivered pre-configured, no changes have to be made to the settings. Pixel clocks up to 93MHz in single-channel LVDS and 186MHz in dual-channel LVDS configuration, and therefore resolutions up to WUXGA (1920x1200) @ 60Hz are supported.

Settings

In order to achieve reliable communication when using longer cables, pre-emphasis may be applied to the LVDS signal lines. On the other hand, the spread spectrum function can enhance the behavior in an EMI sensitive environment.

Mapping

6-bit and 8-bit per color (18-bit or 24-bit color depth) VESA- and JEIDA-mappings are supported at the LVDS-interface. The bit numbering of pixels is big endian, where the most significant bit has the largest bit number. Table 16. LVDS Bit Number Mappings shows the pixel bit number to interface bit number mapping.

Table 16. LVDS Bit Number Mappings

VESA		**JEIDA**	
6-bit	8-bit	6-bit	8-bit
5	7	5	5
4	6	4	4
3	5	3	3
2	4	2	2
1	3	1	1
0	2	0	0
	1		7
	0		6

The following tables are identical for odd and even lines.

Table 17. VESA Data Packing

LVDS Data Line	**Bit position**						
	6	5	4	3	2	1	0
D 0	G0	R5	R4	R3	R2	R1	R0
D 1	B1	B0	G5	G4	G3	G2	G1
D 2	DE	VS	HS	B5	B4	B3	B2
D 3 (for 8-bit)	-	B7	B6	G7	G6	R7	R6

Table 18. JEIDA Data Packing

LVDS Data Line	**Bit position**						
	6	5	4	3	2	1	0
D 0	G2	R7	R6	R5	R4	R3	R2
D 1	B3	B2	G7	G6	G5	G4	G3
D 2	DE	VS	HS	B7	B6	B5	B4
D 3 (for 8-bit)	-	B1	B0	G1	G0	R1	R0

b. I²C Interface

DCMR-64 can control various slave devices via a general purpose I²C-bus interface. Also DCMR-64 can act as slave device and receive display configuration settings. For further information about I²C usage, please contact your local sales partner.

Ambient Light Sensor

DCMR-64 can control an ambient light sensor via an I²C-bus or SMBus interface. Various light sensors are supported which are Plug & Play ready (e.g. TI OPT3001) and therefore do not require additional modifications.

Audio Amplifier

DCMR-64 can control an external audio amplifier with I²C-bus or SMBus interface connected to the Audio Connector (J4).

Electrical Characteristics

DC Characteristics

Table 19. I²C Interface DC Characteristics

Symbol	Characteristic	Test conditions	Min	Typ	Max	Unit
V_{CC}	I²C-bus operating voltage (internal pull-up)	3.3V configuration	3.0	3.3	3.6	V
V_{IH}	High-level input voltage	-	0.7 V_{CC}	-	-	V
V_{IL}	Low-Level input voltage	-	-	-	0.3V_{CC}	V

AC Characteristics

Table 20. I²C Interface AC Characteristics

Symbol	Characteristic	Min	Typ	Max	Unit
f_{CLK}	Clock Frequency	-	100	400	kHz
t_{HIGH}	Clock high time	600	-	-	ns
t_{LOW}	Clock low time	1300	-	-	ns

Operations

In general, the I²C-bus operations must follow the I²C-bus standard.

Write

A write operation looks the following way:

Start	Device Address	Write	Ack	Word Address	Ack	Data	Ack	Stop

Read

A random read operation looks the following way:

Start	Device Address	Write	Ack	Word Address(n)	Ack	Start	Device Address	Read	Ack	Data(n)	No Ack	Stop

c. I²S Interface

DCMR-64 has an I²S interface to handle and forward audio data. This 3-line serial bus consisting of a line for two time-multiplexed data channels (SD), a word select line (WS) and a clock line (SCK), follows the I²S-bus specification. This controller can only act as master and generates the bit clock, word-select signal, and data.

Sampling frequencies of 32kHz to 192kHz are supported.

d. DDC

This LCD controller provides a serial communications link between the video adapter and the controller, the DDC (Display Data Channel). Properties such as maximum resolution, color depth and supported video timing modes can be transmitted using DDC.

EDID

The DCMR-64 controller board supports the Extended Display Identification Data (EDID 1.4) standard.

EDID contains basic information about a monitor and its capabilities, including vendor information, maximum image size, color characteristics, factory pre-set timings, frequency range limits, a character's string for the monitor name and serial number. The video adapter uses this information for configuration purposes, so that the monitor and system can work together.

DDC/CI

The display controller complies with the MCCS 2.2a standard. It supports a standard set of MCCS VCP codes to adjust the displayed image or control the display. Read and write commands are available for the following categories:

- Image Adjustments
- Color Adjustments
- Image Geometry Adjustments
- Audio Adjustments
- Window Operations
- DPVL Support

8. Mechanical Characteristics

2D- (e.g. DXF) & 3D- (e.g. STEP) files for easy product design-in available.

Table 21. Mechanical Characteristics

Parameter	Value
Dimensions (H x V)	150.0mm x 116.0mm (110.0mm PCB + 6.0mm connectors)
Total height (Var. STD)	16.3mm (Top: 12.7mm, PCB: 1.6mm, Bot: 2.0mm)
Contour	Rectangular
Production technology	SMT / THT
Mounting holes	3.5mm (Use M3 screws)
Weight	105 g

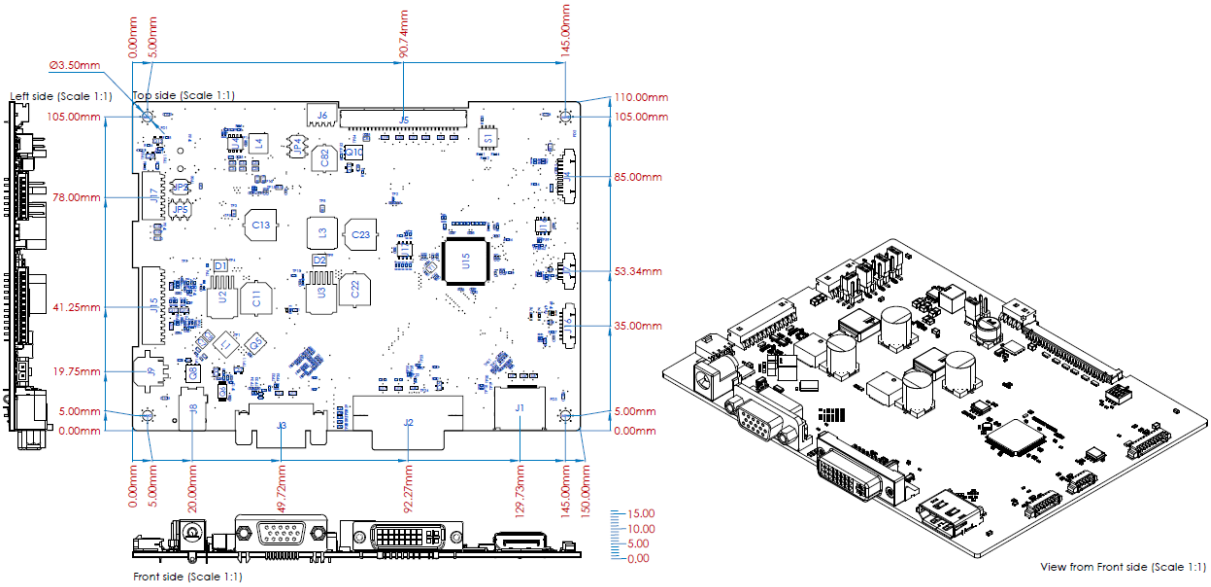


Fig 5. Outline

9. Connectors

Abbreviations used within this chapter are listed in the table below.

Table 22. Signal Assignment Abbreviations

Abbreviation	Description
GND	Ground
PWR	Power
I	Input
O	Output
I/O	Bi-directional
n.c.	Not connected (do not connect)

a. Connector Overview

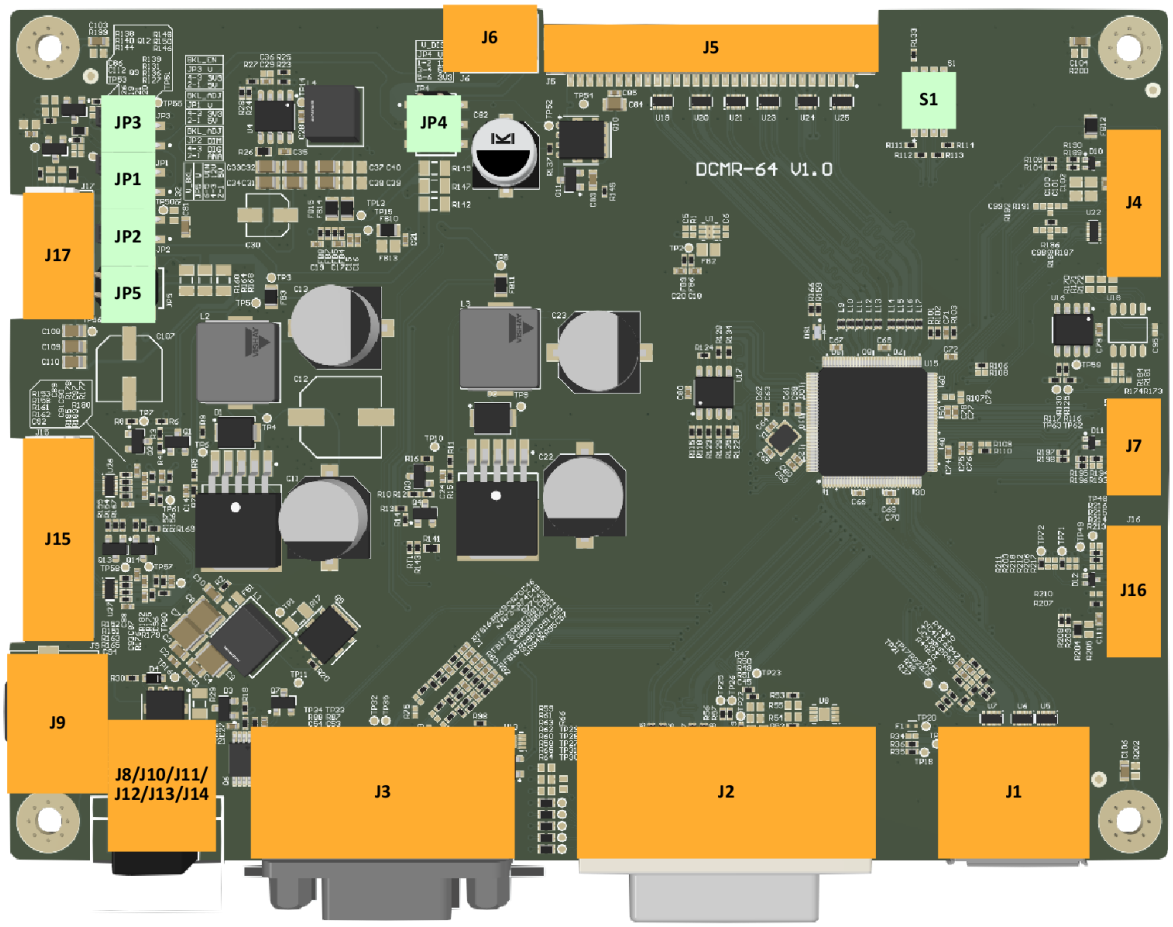


Fig 6. Connector Overview

b. Input Connectors

Power Connectors

The board offers different connector configurations to be supplied with power. In standard configuration, , Power Connector (J8) and Power Connector (J9) are assembled. The other types and combinations are available on request.

Power Connector (J8)

The Power Connector (J8) supplies the board with power. The connection is mandatory.

Table 23. Power Connector (J8)

	Parameter	Value
	Manufacturer: Connector model no.	WE: 694108301002
	Pin amount	3 pins
	Mating housing part	DC Jack 2.5mm x 5.5mm

Note: 5A / 30V max

Table 24. Power Connector (J8) Pin Assignment

Pin	Signal	Description	Type
Inner	VIN	Power input	PWR
Outer	GND	Ground	GND

Power Connector (J12)

The Power Connector (J12) supplies the board with power. The connection is mandatory.

Table 25. Power Connector (J12)

	Parameter	Value
	Manufacturer: Connector model no.	Phoenix contact: 1727010-02
	Pin amount	2 pins (use Pin 1 as marked on PCB)
	Mating housing part	Conductor 1.5mm² / AWG26

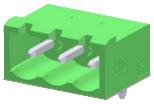
Table 26. Power Connector (J12) Pin Assignment

Pin	Signal	Description	Type
1	GND	Ground	GND
2	VIN	Power input	PWR

Power Connector (J10)

The Power Connector (J10) supplies the board with power. The connection is mandatory.

Table 27. Power Connector (J10)

	Parameter	Value
	Manufacturer: Connector model no.	Phoenix Contact: MSTBA 2,5 HC/ 3-G - 1923762
	Pin amount	3 pins (use Pin 1 as marked in datasheet)

	Mating housing part	Phoenix Contact: MSTB 2,5 HC/ 3-ST - 1911868

Note: Vertical version available.

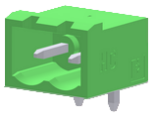
Table 28. Power Connector (J10) Pin Assignment

Pin	Signal	Description	Type
1	VIN	Power input	PWR
2	VIN	Power input	PWR
3	GND	Ground	GND

Power Connector (J13)

The Power Connector (J13) supplies the board with power. The connection is mandatory.

Table 29. Power Connector (J13)

	Parameter	Value
	Manufacturer: Connector model no.	Phoenix Contact: 1923759
	Pin amount	2 pins (use Pin 1 as marked on connector)
	Mating housing part	Phoenix Contact: 1911855

Note: Vertical version available.

Table 30. Power Connector (J13) Pin Assignment

Pin	Signal	Description	Type
1	VIN	Power input	PWR
2	GND	Ground	GND

Power Connector (J9)

The Power Connector (J9) supplies the board with power. The connection is mandatory.

Table 31. Power Connector (J9)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 43650-0212
	Pin amount	2 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 43645-0200

Note: 8.5A max

Table 32. Power Connector (J9) Pin Assignment

Pin	Signal	Description	Type
1	GND	Ground	GND
2	VIN	Power input	PWR

Video Input

The board has different connectors to connect a source, which provides the video signal. In standard configuration all video connectors are assembled.

DisplayPort Connector (J1)

The DisplayPort Connector (J1) can be used to provide a digital video signal to the board. The connection is mandatory.

Table 33. DisplayPort Connector (J1)

	Parameter	Value
	Manufacturer: Connector model no.	Standard DisplayPort receptacle
	Pin amount	20 pins
	Mating housing part	Standard DisplayPort plug

Table 34. DisplayPort Connector (J1) Pin Assignment

Pin	Signal	Description	Type
1	ML_Lane 3 (n)	Main-Link lane 3-	I
2	GND	Ground	GND
3	ML_Lane 3 (p)	Main-Link lane 3+	I
4	ML_Lane 2 (n)	Main-Link lane 2-	I
5	GND	Ground	GND
6	ML_Lane 2 (p)	Main-Link lane 2+	I
7	ML_Lane 1 (n)	Main-Link lane 1-	I
8	GND	Ground	GND
9	ML_Lane 1 (p)	Main-Link lane 1+	I
10	ML_Lane 0 (n)	Main-Link lane 0-	I
11	GND	Ground	GND
12	ML_Lane 0 (p)	Main-Link lane 0+	I
13	Config 1	-	-
14	Config 2	-	-
15	AUX CH (p)	AUX-CH+	I/O
16	GND	Ground	GND
17	AUX CH (n)	AUX-CH-	I/O
18	HPD	Hot-plug detect	O
19	PWR Return (GND)	Ground	GND
20	PWR (3.3V)	Power	PWR

DVI-I connector (J2)

The DVI-I connector (J2) can be used to provide a digital video signal to the board. The connection is mandatory.

Table 35. DVI-I connector (J2)

	Parameter	Value
	Manufacturer: Connector model no.	Standard DVI-I (Dual Link) receptacle
	Pin amount	24+5 pins
	Mating housing part	Standard DVI-I (Dual Link) plug

Table 36. DVI-I connector (J2) Pin Assignment

Pin	Signal	Description	Type
1	D2-	T.M.D.S. Data2-	I
2	D2+	T.M.D.S. Data2+	I
3	GND	T.M.D.S. Data2/4 shield	GND
4	n.c.	-	n.c.
5	n.c.	-	n.c.
6	SCL	DDC clock	I/O
7	SDA	DDC data	I/O
8	A_VS	Analog Vertical Sync	I
9	D1-	T.M.D.S. Data1-	I
10	D1+	T.M.D.S. Data1+	I
11	GND	T.M.D.S. Data1/3 shield	GND
12	n.c.	-	n.c.
13	n.c.	-	n.c.
14	VCC	+5V Power	PWR
15	GND	Ground (return for +5V, HSync and VSync)	GND
16	HPD	Hot-plug detect	I
17	D0-	T.M.D.S. Data0-	I
18	D0+	T.M.D.S. Data0+	I
19	GND	T.M.D.S. Data0/5 shield	GND
20	n.c.	-	n.c.
21	n.c.	-	n.c.
22	GND	T.M.D.S. Clock shield	GND
23	RXC+	T.M.D.S. Clock+	I
24	RXC-	T.M.D.S. Clock-	I
C1	A_RD	Analog Red	I
C2	A_GN	Analog Green	I
C3	A_BL	Analog Blue	I
C4	A_HS	Analog Horizontal Sync	I
C5	A_GND	Analog Ground (analog R, G, & B return)	GND

VGA connector (J3)

The VGA connector (J3) can be used to provide an analog RGB signal to the board. The connection is mandatory.

Table 37. VGA connector (J3)

	Parameter	**Value**
	Manufacturer: Connector model no.	Standard: HD D-Sub DE-15 receptacle
	Pin amount	15 pins (use Pin 1 as marked on connector)
	Mating housing part	Standard: HD D-Sub DE-15 plug

Table 38. VGA connector (J3) Pin Assignment

Pin	Signal	Description	Type
1	R0+	Red signal	I
2	G0+	Green signal	I

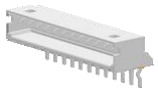
3	B0+	Blue signal	I
4	n.c.	-	n.c.
5	GND	Ground	GND
6	R0-	Red ground	I
7	G0-	Green ground	I
8	B0-	Blue ground	I
9	VCC	+5V Power	PWR
10	GND	Ground	GND
11	n.c.	-	n.c.
12	SDA	DDC data	I/O
13	AHS	Horizontal sync	I
14	AVS	Vertical sync	I
15	SCL	DDC clock	I/O

c. I/O Connectors

OSD Interface Connector (J15)

The OSD Interface Connector (J15) can be used to connect an OSD-keyboard. The connection is optional.

Table 39. OSD Interface Connector (J15)

	Parameter	**Value**
	Manufacturer: Connector model no.	Molex: 53015-1210
	Pin amount	12 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51004-1200

Note: Max 1A per contact

Table 40. OSD Interface Connector (J15) Pin Assignment

Pin	**Signal**	**Description**	**Type**
1	KEY_RT	OSD key right	I
2	KEY_LT	OSD key left	I
3	GND	Ground	GND
4	KEY_EXT	OSD key exit	I
5	KEY_MNU	OSD key menu	I
6	LED_RD	LED red	O
7	5V	OSD power	O
8	LED_GN	LED green	O
9	GND	Ground	GND
10	POWER	OSD key power	I
11	n.c.	Reserved for internal use (do not connect)	n.c.
12	GND	Ground	GND

GPIO Connector (J16)

The GPIO Connector (J16) is used to attach external peripherals. The connection is optional.

Table 41. GPIO Connector (J16)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 53261-0871
	Pin amount	8 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51021-0800

Note: Max 1A per contact

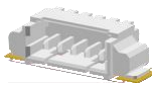
Table 42. GPIO Connector (J16) Pin Assignment

Pin	Signal	Description	Type
1	V_OUT	Output Voltage (Typ. 3.3V)	O
2	GND	Ground	GND
3	ADC1 / GPIO	Analog to digital converter	I/O
4	ADC2 / GPIO	Analog to digital converter	I/O
5	SCL / RX / GPIO	I2C Clock / UART Receive / GPIO	I/O
6	SDA / TX /GPIO	I2C Data / UART Transmit / GPIO	I/O
7	PWM1 / GPIO	Pulse Width Modulation Output / GPIO	I/O
8	PWM2 / GPIO	Pulse Width Modulation Output / GPIO	I/O

I²C Connector (J7)

The I²C Connector (J7) can be to connect an external I²C slave device. The connection is optional.

Table 43. I²C Connector (J7)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 53261-0571
	Pin amount	5 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51021-0500

Note: Max 1A per contact

Table 44. I²C Connector (J7) Pin Assignment

Pin	Signal	Description	Type
1	5V	5V output	O
2	3V3	3.3V output	O
3	SDA	I2C Data	I/O
4	SCL	I2C Clock	I/O
5	GND	Ground	GND

d. Output Connectors

LVDS Interface (J5)

The LVDS Interface (J5) is the video output interface. The connection is mandatory.

Table 45. LVDS Interface (J5)

	Parameter	Value
	Manufacturer: Connector model no.	Hirose: DF14-30P1.25H
	Pin amount	30 pins (use Pin 1 as marked on PCB)
	Mating housing part	Hirose: DF14-30S-1.25C

Note: Max 1A per contact

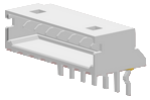
Table 46. LVDS Interface (J5) Pin Assignment

Pin	Signal	Description	Type
1	GND	Ground	GND
2	TXE3+	Positive LVDS differential data output – Line 3 (even)	O
3	TXE3-	Negative LVDS differential data output – Line 3 (even)	O
4	TXECLK+	Positive LVDS differential data output (even)	O
5	TXECLK-	Negative LVDS differential data output (even)	O
6	TXE2+	Positive LVDS differential data output – Line 2 (even)	O
7	TXE2-	Negative LVDS differential data output – Line 2 (even)	O
8	TXE1+	Positive LVDS differential data output – Line 1 (even)	O
9	TXE1-	Negative LVDS differential data output – Line 1 (even)	O
10	TXE0+	Positive LVDS differential data output – Line 0 (even)	O
11	TXE0-	Negative LVDS differential data output – Line 0 (even)	O
12	GND	Ground	GND
13	TXO3+	Positive LVDS differential data output – Line 3 (odd)	O
14	TXO3-	Negative LVDS differential data output – Line 3 (odd)	O
15	TXOCLK+	Positive LVDS differential clock output (odd)	O
16	TXOCLK-	Negative LVDS differential clock output (odd)	O
17	TXO2+	Positive LVDS differential data output – Line 2 (odd)	O
18	TXO2-	Negative LVDS differential data output – Line 2 (odd)	O
19	TXO1+	Positive LVDS differential data output – Line 1 (odd)	O
20	TXO1-	Negative LVDS differential data output – Line 1 (odd)	O
21	TXO0+	Positive LVDS differential data output – Line 0 (odd)	O
22	TXO0-	Negative LVDS differential data output – Line 0 (odd)	O
23	GND	Ground	GND
24	GND	Ground	GND
25	GND	Ground	GND
26	V_PNL	Panel Power	O
27	V_PNL	Panel Power	O
28	V_PNL	Panel Power	O
29	V_PNL	Panel Power	O
30	V_PNL	Panel Power	O

Backlight Connector (J17)

The Backlight Connector (J17) is used to power backlight units. The connection is mandatory.

Table 47. Backlight Connector (J17)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 53015-0710
	Pin amount	7 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51004-0700

Note: Max 1A per contact

Table 48. Backlight Connector (J17) Pin Assignment

Pin	Signal	Description	Type
1	GND	Ground	GND
2	GND	Ground	GND
3	GND	Ground	GND
4	BKL_ADJ	Backlight Adjust	O
5	BKL_EN	Backlight Enable	O
6	V_BKL	Backlight Power	O
7	V_BKL	Backlight Power	O

Audio Connector (J4)

The Audio Connector (J4) can be used to attach external audio periphery. A DAC might be connected by I²S. The connection is optional.

Table 49. Audio Connector (J4)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 53261-1071
	Pin amount	10 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51021-1000

Note: Max 1A per contact

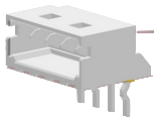
Table 50. Audio Connector (J4) Pin Assignment

Pin	Signal	Description	Type
1	MCK	Master Clock	O
2	SCK	Serial Clock	O
3	WS	Word Select	O
4	SD0 / SOUT_L	Serial Data / Speaker out left	O
5	GND	Ground	GND
6	SOUT_R	Speaker out right	O
7	GND	Ground	GND
8	V_OUT	Output Voltage (Typ. 3.3V)	O
9	SCL	I2C Clock	I/O
10	SDA	I2C Data	I/O

External Power Supply (J6)

The External Power Supply (J6) offers different voltage levels for powering additional components. The connection is optional.

Table 51. External Power Supply (J6)

	Parameter	Value
	Manufacturer: Connector model no.	Molex: 53015-0410
	Pin amount	4 pins (use Pin 1 as marked on connector)
	Mating housing part	Molex: 51004-0410

Note: Max 1A per contact

Table 52. External Power Supply (J6) Pin Assignment

Pin	Signal	Description	Type
1	VEXT_12	12V power output	O
2	VEXT_5	5V power output	O
3	GND	Ground	GND
4	GND	Ground	GND

10. Environmental Ratings

Table 53. Environmental ratings

Symbol	Test item	Min	Max	Unit
ϑ_{ST}	Storage temperature	-20	70	°C
ϑ_{OP}	Operating temperature[10]	0	60	°C

11. Absolute Maximum Ratings

Table 54. Absolute maximum ratings

Symbol	Test item	Min	Max	Unit
ϑ_{ST}	Storage temperature	-35	70	°C
ϑ_{OP}	Operating temperature	-10	65	°C

12. Application Information

a. User's Manual

For configuration of the assembly variant "STD", please check the User's Manual.

b. Operating Precautions

- Be sure to ground yourself before handling the controller board.
- Turn off power supply before inserting or disconnecting any connector.
- Spike noise causes maloperation of circuits. It should be lower than following voltage: ±200mV (over and under shoot voltage).
- This module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

c. General Cautions

- The responsibility for the applicability of customer specific products and use in a particular customer design is always within the authority of the customer.
- An important factor for each system integration is the thermal design. System designers might need to implement a passive or active cooling system in their specific design to keep the temperatures of all parts within the specification.
- Be careful of condensation at sudden temperature change. Condensation damages electrical contacted parts.

[10] Phase- or clock-shift can appear between -10°C and 0°C

- When preparing a cable for a specific display, always refer to appropriate cable pin-out and display specification. Always check the signals before connecting the cable. Any incorrect pin connection may damage the display and the controller permanently.
- Take care of all the sensitive electronic components
- Do not modify the module assembly
- You must mount a module using its respective mounting holes and avoid any bend force during mounting.
- Be sure to do a reset in OSD if any problem occurs during operation
- Should you need any technical help, please contact distronik GmbH

d. Status LED

DCMR-64 has an on-board LED which can be used to check the board's status without a display attached. More detailed information will be printed as on-screen-message on the display (if attached). LED colors and their meanings are described in the table below.

Table 55. On-board LED status table

LED Color	Represents	User Actions
Off	Incorrect power supply	Check power supply
Green	Normal state	n/a
Red	Input signal/cable error	Check cable and input signal

13. Ordering Information

This board is available in different configurations which can be selected as best suitable for your application. The predefined hardware configurations of are available. If another assembly configuration or special FW settings (e.g. default values, boot logo, etc.) suits your needs better, please contact distronik GmbH for a customized configuration.

Table 56. Variant selection table

#	Var.	V_{IN}	DisplayPort	DVI-I	Analog RGB	Reverse voltage protection	GPIOs	Audio
1	STD	12V-24V	✓	✓	✓	✓	✓	✓
2	Lite, 24V	15-24V	✓	✓	✓	×	×	×
3	Lite, 12V	12V	✓	✓	✓	×	×	×

14. Packaging / Labels

a. Part

Serial number

Serial number looks in general the following way:

AAAN.N-YYMMDDXXXX

Table 57. S/N Encoding

Code	Meaning
AAA	Acronym for the SMT producer
N.N	Firmware version
YYMMDD	Manufacturing date (YY/MM/DD)
XXXX	Manufacturing sequence of product

Label

Part label will look similar to the figure below.

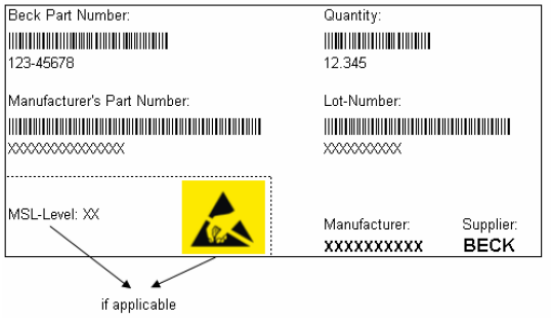


Fig 7. Part label

ESD bag

Each board will be packed in a conductive ESD bag.

b. Packaging

Label

The outer label will be similar to the figure below.

Fig 8. Box label

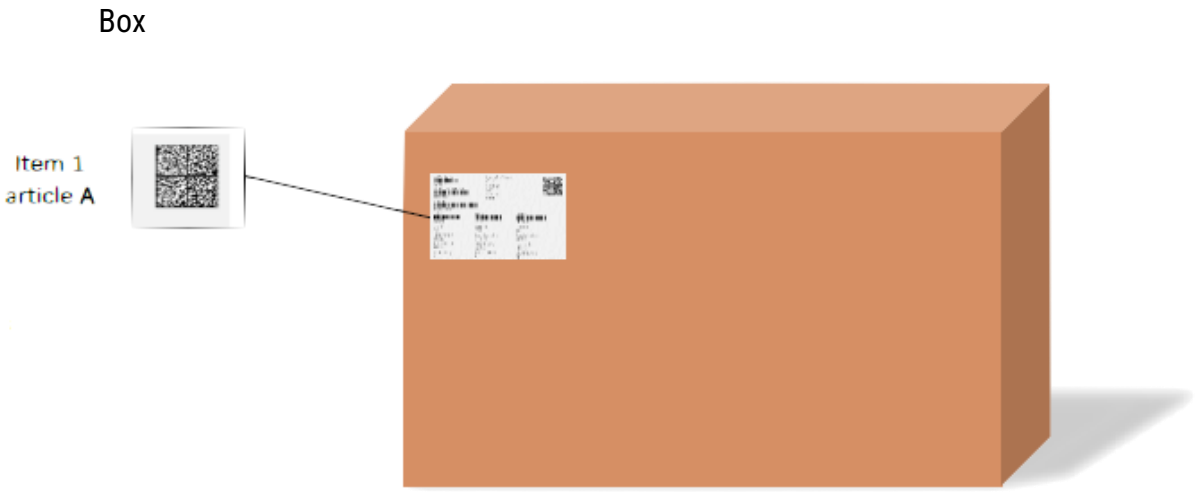


Fig 9. Carton

Table 58. Packaging details

Name	Value
Max capacity	84 pcs per carton
Max. weight	11.0 kg per carton
Outline dimension of carton	530 mm x 350 mm x 300 mm

15. Abbreviations

Table 59. Abbreviations

Abbr.	Description
ALS	Ambient Light Sensor
BLU	Backlight Unit
BPC	Bits Per Color
BPP	Bits Per Pixel
DAC	Digital-to-analog Converter
DDC	Display Data Channel
DDC/CI	Display Data Channel Command Interface
DDWG	Digital Display Working Group
DPLL	Digital Phase-Locked Loop
DPMS	Display Power Management Service
DVI-I	Digital Visual Interface - Integrated
EDID	Extended Display Identification Data
EEPROM	Electrically Erasable Programmable Read-Only Memory
EMI	Electromagnetic Interference
ESD	Electrostatic Discharge
GND	Ground
HDCP	High Digital Content Protection
HDMI	High Definition Multimedia Interface
I/O	Input / Output
I²C	Inter Integrated Circuit
I²S	Inter-IC Sound
ISP	In System Programming
JEIDA	Japan Electronic Industry Development Association
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LVDS	Low Voltage Different Signaling
MCCS	Monitor Command Control Set
OSD	On Screen Display

PCB	Printed Circuit Board
PCLK	Pixel Clock
PWM	Pulse Width Modulation
REACH	Registration, Evaluation, Authorization and Restriction of Chemicals
RGB	Red, Green, Blue
RoHS	Restriction of Hazardous Substances
SMBus	System Management Bus
SMT	Surface Mounted Technology
SOG	Sync-On-Green
sRGB	Standard-RGB
TFT	Thin-Film Transistor
TMDS	Transition-Minimized Different Signaling
VCP	Virtual Control Panel
VESA	Video Electronics Standards Association

16. Revision History

Table 60. Revision History

Rev.	Date	Section	Specification Status	Description
-	Jun 30th, 2022	All	Final	Initial release

17. Legal Information

a. Disclaimers

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Stress above one or more limiting values (as defined in section Absolute maximum ratings) may cause permanent damage and irreversibly affect the quality and reliability of the device.

b. Trademarks

All referenced brands, product names, service names and trademarks are the property of their respective owners.

18. Tables

19. Figures

20. Contents

21. Contact Information

For more information or technical support, please visit our website: www.distronik.de